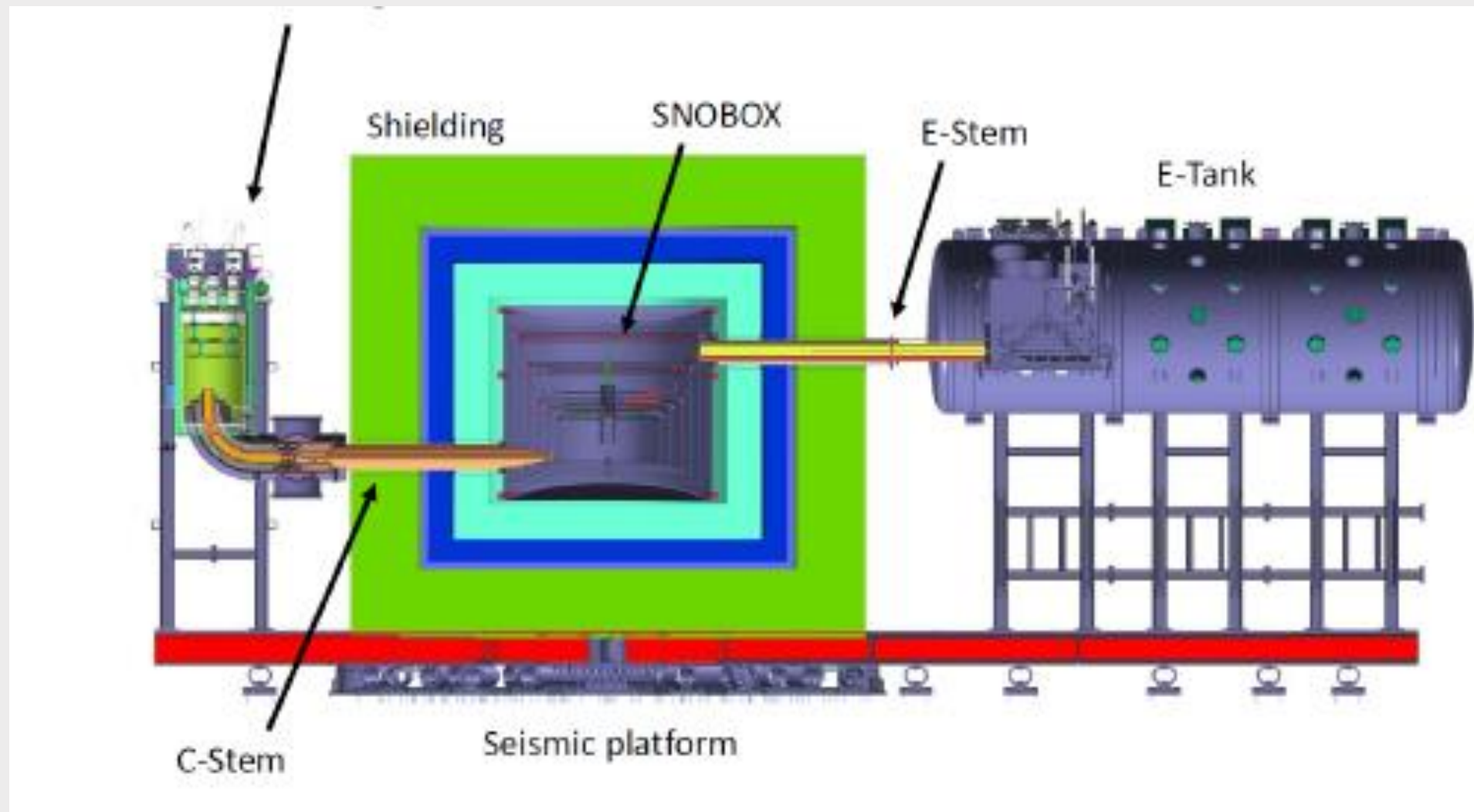
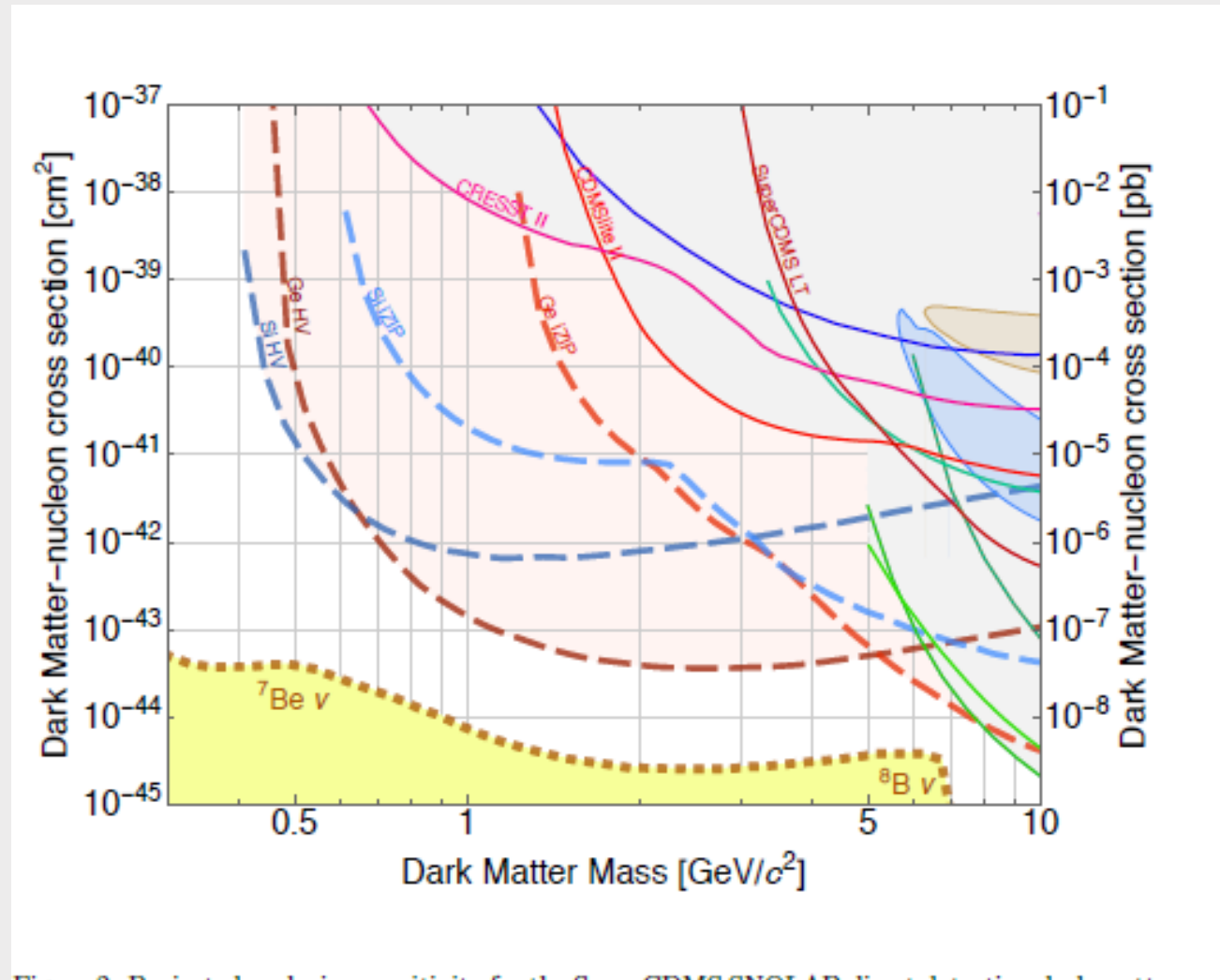


SuperCDMS SNOLAB Data Acquisition System

The SuperCDMS Experiment



References, Acknowledgements

Data Acquisition System

Design Requirements

Framework

ODB

DCRC Driver

Pulse Display

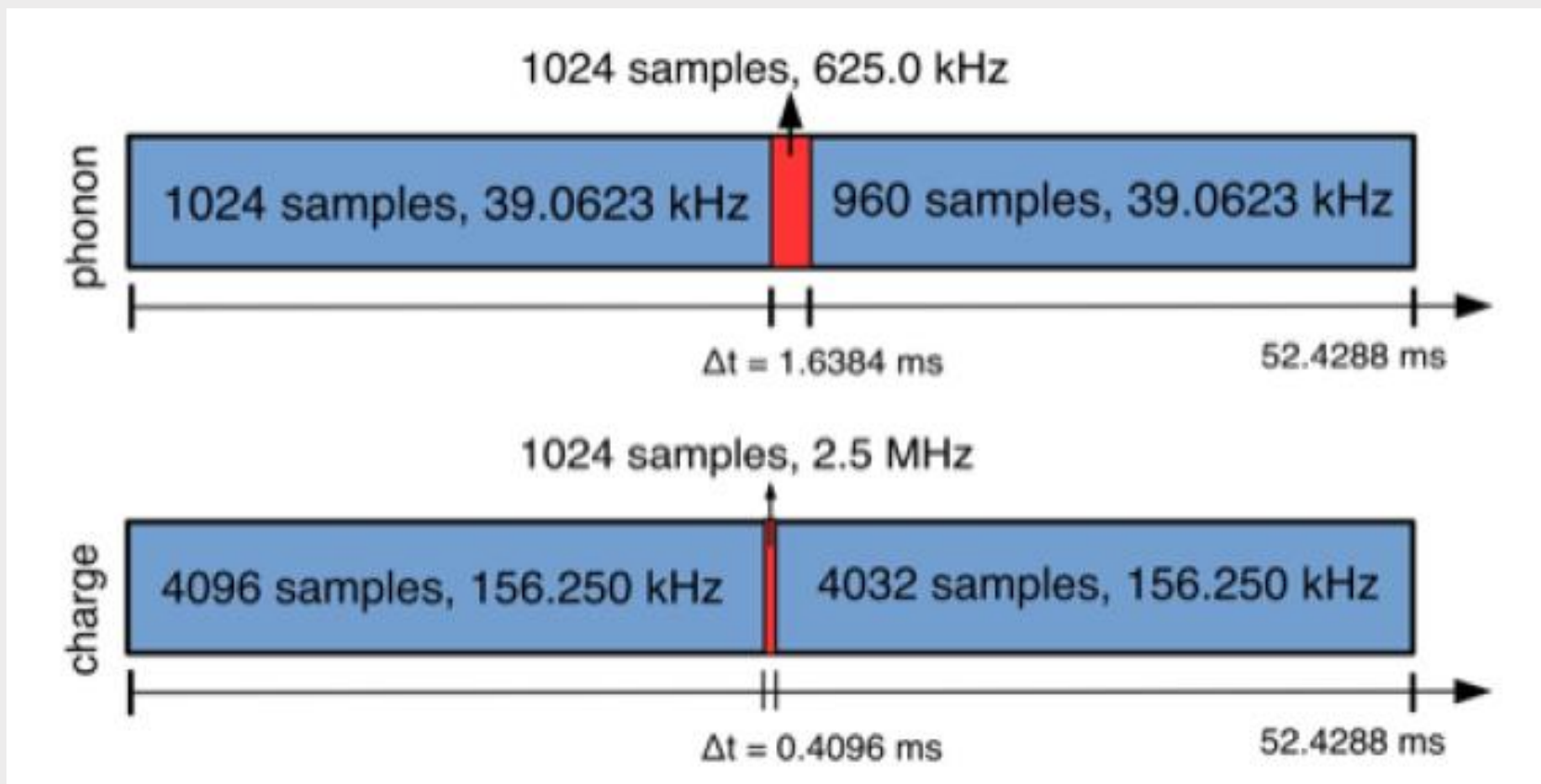
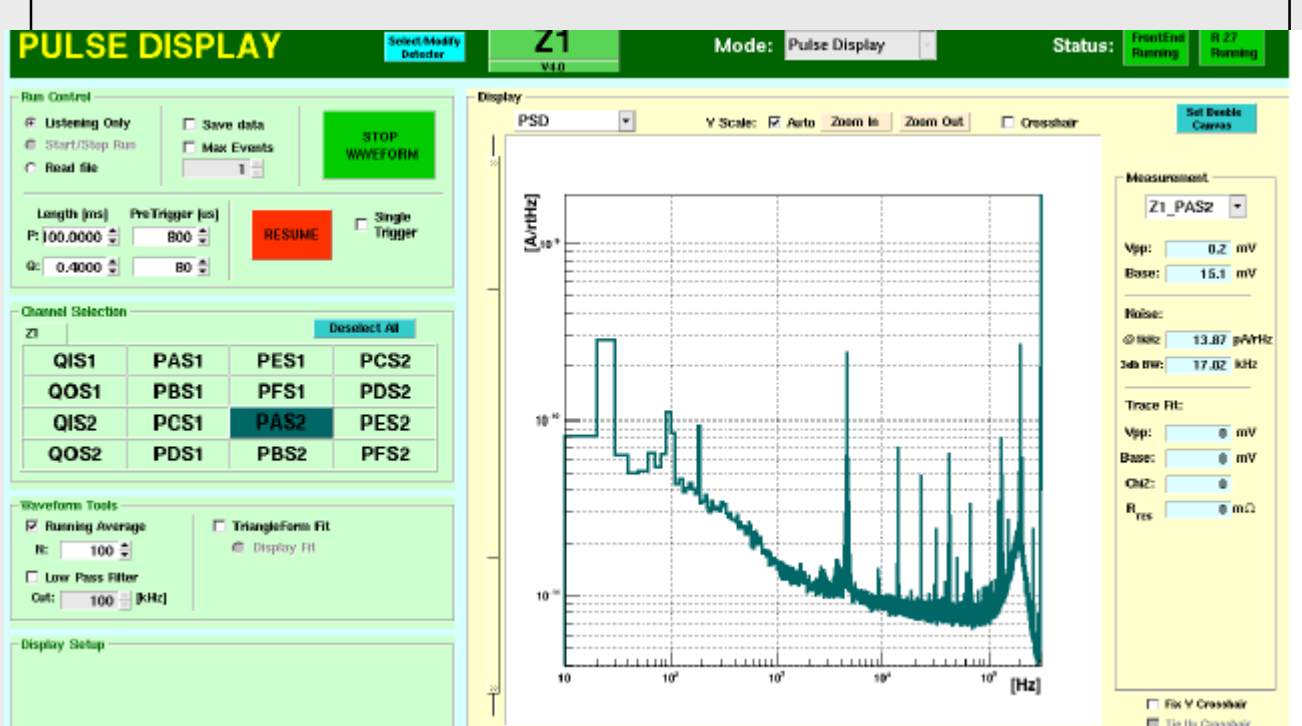
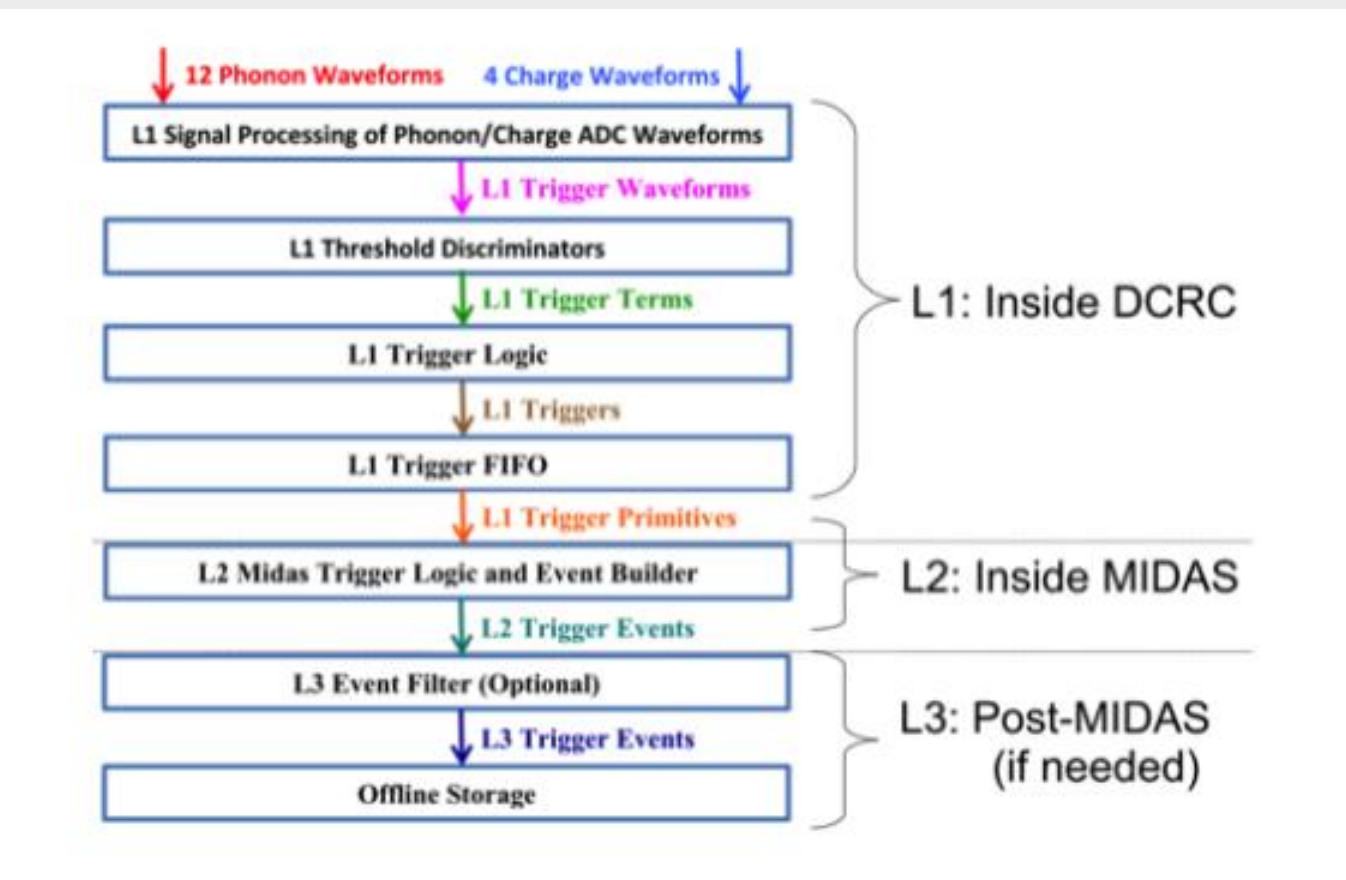


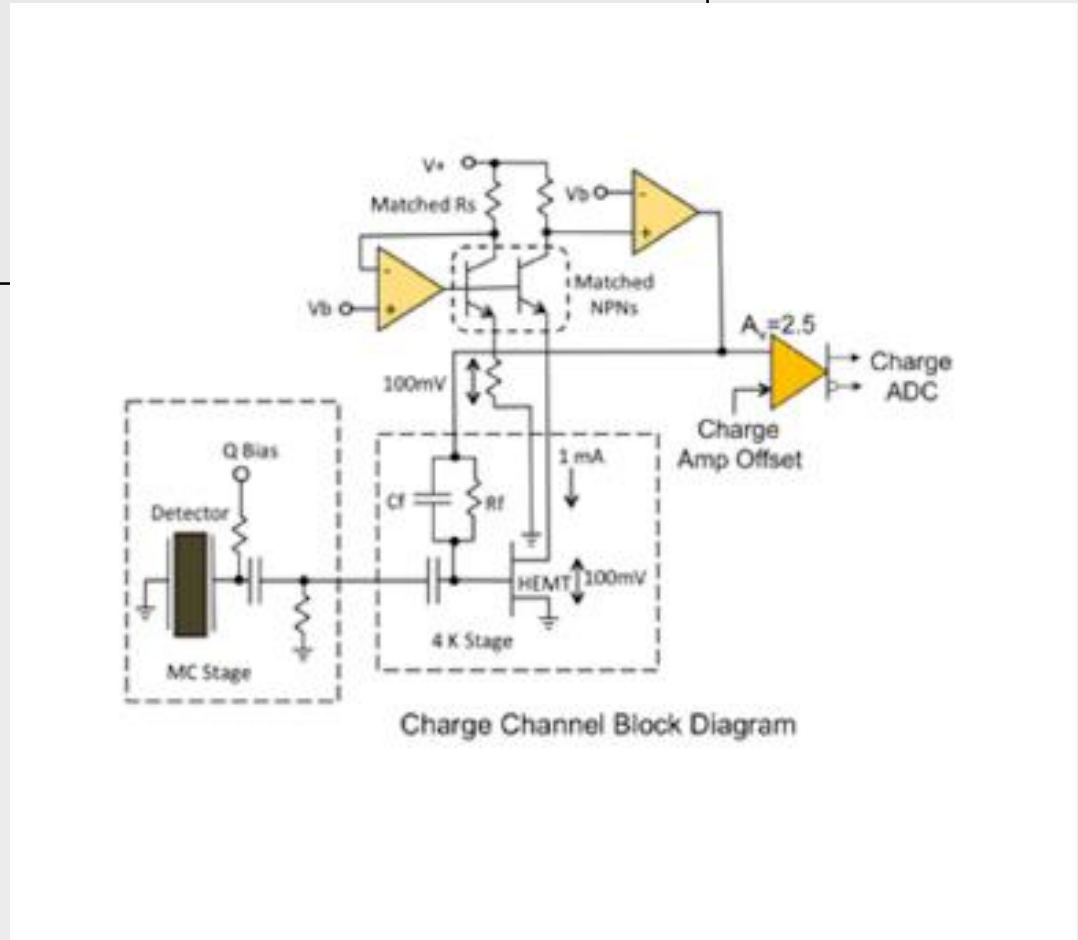
Figure 197: Sampling rates in the raw pulse rates and post-pulse rates for phonon and charge

Trigger System

- L1
- L2
- L3



DAQ Hardware?



Environmental Monitoring